

FIGURE 1B

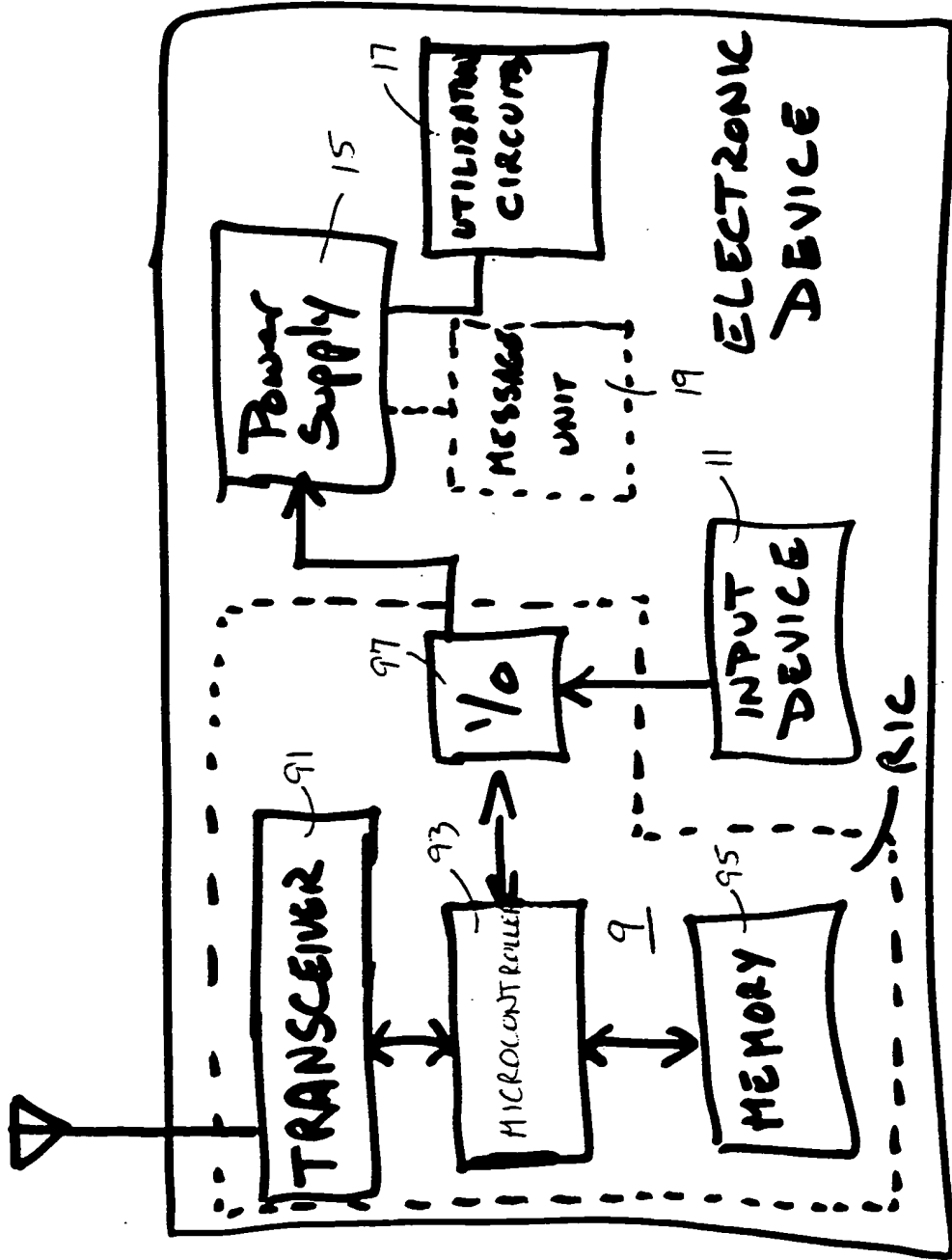
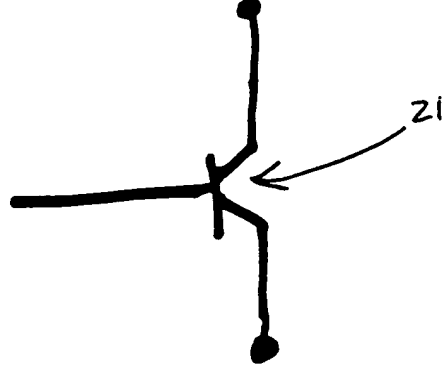


FIGURE 2

CONTROL
FROM
CPU

FROM
POWER
SOURCE



TO UTILIZATION
DEVICE

FIGURE 3A

662240" 94936260

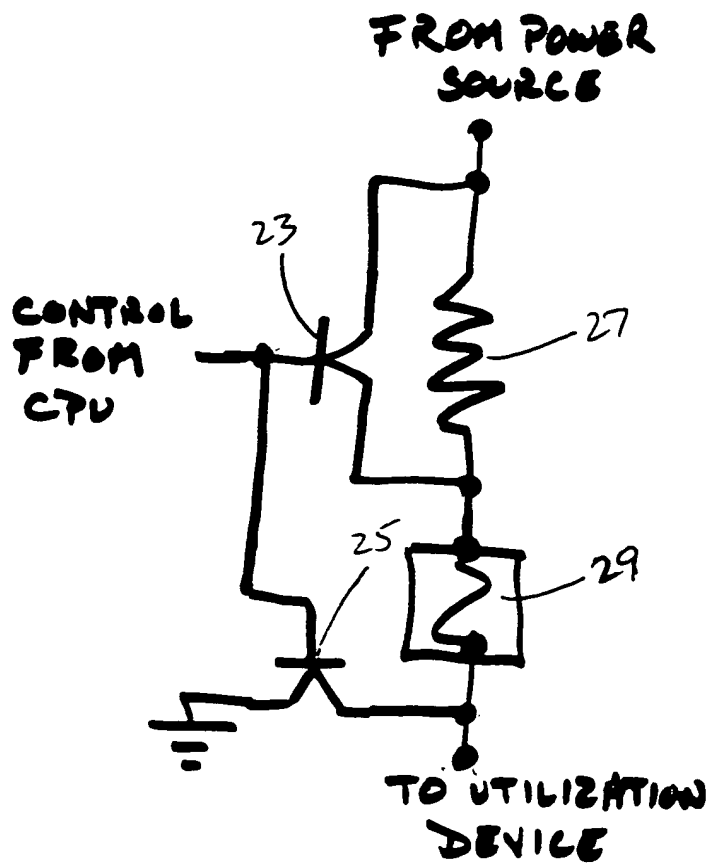


FIGURE 3B

662210" 92996260

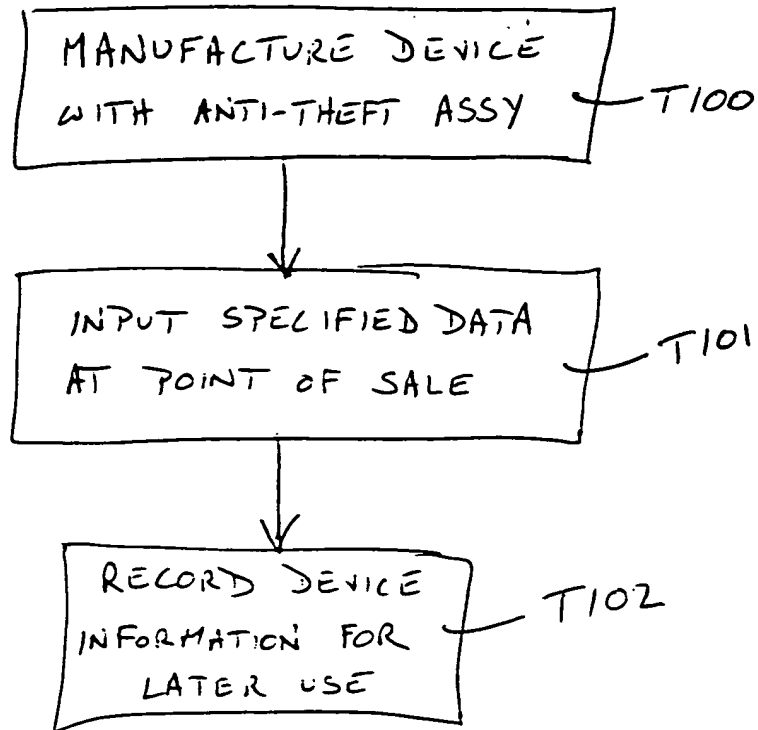
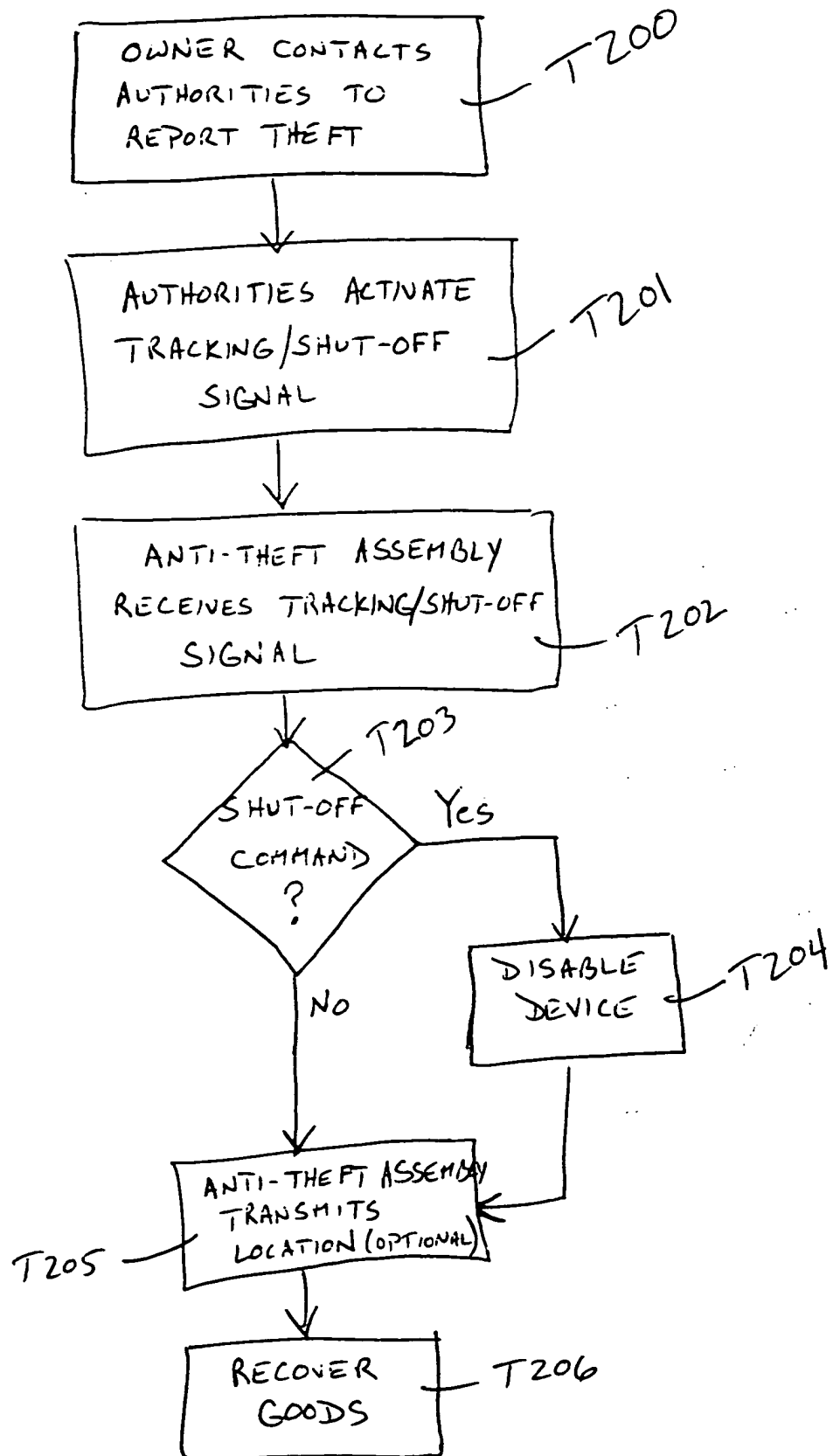


FIGURE 4

652210" 9/9/96 260

FIGURE 5



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